



# RAID HIPICO ELVAS

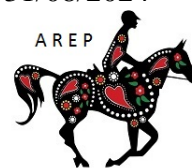
CEI1\* 100 / 100 Km.

José Luis Balsinhas

Djinna De Breyt

Lic: 10096691 / Ldn: 108JR91

31/08/2024



Nº participantes: 10    Clasificados: 5 (50%)    No clasificados: 5 (50%)

	Phase I	Phase II	Phase III	Phase IV	Phase V	Phase VI
kilometers	30,000	30,000	20,000	20,000		
Departure	07:00:00	09:47:30	12:39:20	14:30:40		
Arrival Time	09:03:24	11:50:05	13:45:50	15:36:02		
Time	02:03:24	02:02:35	01:06:30	01:05:22		
Speed in Arri.	14,586	14,683	18,045	18,357		
Accumu. Time	02:03:24	04:05:59	05:12:29	06:17:51		
Accumu. Speed	14,586	14,635	15,36	15,879		
Arrival Vet-in.	09:07:30	11:59:20	13:50:40	15:43:00		
Vet-in Time	02:07:30	02:11:50	01:11:20	01:05:22		
Vet-in Speed	14,117	13,653	16,822	18,357		
Accumu. Time Vet	02:07:30	04:19:20	05:30:40	06:36:02		
Accumu Speed	14,117	13,881	14,516	15,15		
Recovery	00:04:06	00:09:15	00:04:50	00:06:58		
Accu. Recovery	00:04:06	00:13:21	00:18:11			
Heart Rate						
Into a hold	00:40:00	00:40:00	00:40:00			
Color:	Yellow	Yellow	Red	Red		
Close:				16:08:34		
Rank:	9	6	6	5		

		RAID HIPICO ELVAS (CEI1* 100)		Date:	31/08/2024	Number	12								
		10096691 José Luis Balsinhas / 108JR91 Djinna De Breyt		Weight:	70 Kg.										
Parameters	Pulse 1	Pulse 2	Skin t.	Mucous	Capilar	Gut s.	Girth	Gait	Impuls	Remark	Signature				
Pre-ride inspection	36	40	2	A	4	A	A	A	A	OK	MS				
V-Gate 1	64	60	2	B	2	B	A	A	A	OK	MS				
1st inspection															
Re-inspection															
Re-exam															
V-Gate 2	54	64	2	B	2	B	A	A	A	OK	MS				
1st inspection															
Re-inspection															
Re-exam															
V-Gate 3	65	60	2	A	A	A	A	A	A	OK	MS				
1st inspection															
Re-inspection															
Re-exam															
V-Gate 4															
1st inspection															
Re-inspection															
Re-exam															
V-Gate 5															
1st inspection															
Re-inspection															
Re-exam															
Final inspection	34	65	2	A	4	A	B	A	A	OK	MS				
No Classification codes	WD	DSQ	RET	FNR	FTO	GA	ME	MI	CI	OT	FTC	TR	SI-Mu	SI-Me	SI-Or
Num Gate															

